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BIB DATA SHEET

CONFIRMATION NO. 7370

SERIAL NUMBER		CLASS	GROUP ART UN	IIT ATTO	DRNEY DOCKET	
09/915,437	DATE 07/26/2001	326	2825		NO. 956.1061	
	RULE					
APPLICANTS Sang Hoo Dhong, Austin, TX; Harm Peter Hofstee, Austin, TX; Stephen Douglas Posluszny, Round Rock, TX; Joel Abraham Silberman, Somers, NY; Osamu Takahashi, Round Rock, TX; Dieter F. Wendel, Schoenaich, DE; ** CONTINUING DATA **********************************						
	Yes No met Yes No C TAT/ ner's Signature No BT Initials	STATE OR COUNTRY TX		TOTAL CLAIMS	INDEPENDENT CLAIMS	
ADDRESS THE CULBERTSON GROUP, P.C. 1114 LOST CREEK BLVD. SUITE 420 AUSTIN, TX 78746 UNITED STATES						
TITLE						
Method of logic circuit synthesis and design using a dynamic circuit library						
			☐ All Fees			
 	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT		☐ 1.16 Fees	☐ 1.16 Fees (Filing)		
I FII 11413 FFF 1			NT 1.17 Fees	☐ 1.17 Fees (Processing Ext. of time)		
710 No	No for following:			☐ 1.18 Fees (Issue)		
				Other		
		☐ Credit	☐ Credit			